



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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FEB 25 2004

Applicants : Mimberg, et al.

Serial No. : 10/078,292

Examiner: Sun, X.

Filed : 2/15/02

Group Art Unit: 2863

For : A METHOD AND SYSTEM FOR DYNAMIC POWER SUPPLY
VOLTAGE ADJUSTMENT FOR A SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE

RESPONSE TO FINAL OFFICE ACTION

Assistant Commissioner for Patents & Trademarks
Washington, D.C. 20231

Sir:

In response to the Office Action mailed 11/12/03, please consider the following
declaration in accordance with 37 C.F.R. 1.131 and the accompanying remarks: